

CURRICULUM VITAE

Dr.P.Ramadevi
Associate Professor
Department of ECE
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➤ Research Area:

- VLSI
- Wireless Network Security
- AI based Cybersecurity

➤ Educational Qualifications:

- Ph.D. (VLSI), Anna University, Chennai in the year 2017
- M.Tech.SE, School of Engineering and Technology, Bharathidasan University, 2008
- B.E. Electronics and Communication Engineering, P.S.G. College of Technology, Bharathiyar University, 1993
- Diploma in Electronics and Communication Engineering, 1989.

Experience:

- Associate Professor, University college of Engineering BIT Campus Tiruchirappalli, 28-06-2017 to till date.
- Assistant Professor, University college of Engineering BIT Campus Tiruchirappalli, 06-03-2003 to 28-06-2017
- Contract Lecturer (DOTE), Alagappa Polytechnic College Karaikudi, 06-10-1998 to 04-03-2003
- Saranathan College of Engineering Trichy, 03-01-2000 to 19-12-2000
- Srimathi Indra Gandhi College Trichy, Srinivasa Polytechnic College Keeranur 01-07-1996 to 06-10-1998
- Engineer and Part-time Lecturer, BHEL Trichy, 18-05-1995 to 30-06-1996
- Engineer, RK metal Industries Trichy 05-01-1993 to 17-05-1995

➤ **Awards / Honours Received:**

100% Results Honor Received

➤ **Additional / Academic Responsibilities at University:**

- ECE Course Coordinator (From 2003 to 2009)
- Head of the department in charge (2012 to 2019)
- Officer in - charge of National Service Scheme (2003 to 2006)
- Chief superintendent of examinations (2015 to 2016)
- Member - board of studies (2006 to 2008)
- AU CAI inspection affiliation Committee member (2017 to 2023)
- Centre for research constitution of inspection committee member
- Purchase committee member TEQIP BIT Trichy

- Graduation day committee member prizes awards and medals
- ECE association
- Camp officer for University Central Valuation
- Member of Malpractice Committee

➤ **Membership of Scientific and Professional Societies:**

The Institute of Engineer (India)

➤ **Completed PG/UG Projects:**

- Number of PG Projects Completed: 7 Nos
- Number of UG Project Completed: 40 batches

➤ **Publications:**

- **Ramadevi, P.**, T. Jayasankar, V. Dinesh, and M. Dhamodaran. "Chaotic Sandpiper Optimization Based Virtual Machine Scheduling for Cyber-Physical Systems." *Computer Systems Science & Engineering* 44, no. 2 (2023). 1373-1385
- Ramachandran, A., **P. Ramadevi**, Ahmed Alkhayyat, and Yousif Kerrar Yousif. "Blockchain and Data Integrity Authentication Technique for Secure Cloud Environment." *Intelligent Automation and Soft Computing* 36, no. 2 (2023): 2055-2070.
- **Ramadevi, P.**, et al. "Deep Learning Based Distributed Intrusion Detection in Secure Cyber Physical Systems." *Intelligent Automation & Soft Computing* 34.3 (2022).

- **P. Ramadevi**, B.Manikandan, T.Jayasankar. “An Implementing a continuous Authentication Protocol to Improve Robustness Security Threats on IOT using ESP8266.” Recent and Innovation Trends in Computing and Communication (2022).
- **P. Ramadevi**,. “A Novel Implementation of Elliptic Curve Cryptography for Security Purpose.” International Journal of Control and Automation (2019).
- **P. Ramadevi**,. “Fuzzy based Dynamic Encryption Algorithm for WLAN Security Enhancement.” Journal of Advanced Science and Technology (2019).
- Geetharamani, G & **Ramadevi, P** 2016, ‘FPGA Implementation of Fuzzy Based Twofish Encryption Algorithm in Wireless Network’ Transylvanian Special Issue Review vol. 24, no. 10.
- Geetharamani, G & **Ramadevi, P** 2016 ‘Realization of Fuzzy Logic Controller using VHDL in Wireless Network Environment’ Global Journal of Engineering Science and Researches 211 ISSN 2348 – 8034 DOI-10.5281/zenodo.154679 Impact Factor- 4.022.
- Geetharamani, G & **Ramadevi, P** 2016, ‘Performance analysis of FPGA implementation of enhanced Twofish encryption algorithm’ International Journal of Application or Innovation in Engineering and Management ISSN 2319 - 4847.
- Dr.G.Geetharamani, **Ramadevi.P**, M.Ganimohi, 2016 “ VHDL Implementation of Safer Plus Algorithm ” TEQIP sponsored National Conference on Nascent Technologies in signal processing and communication.

- Prema R, **Ramadevi P**, 2014, “Synthesis of Synchronization for Design of Multiband Approach in High Data Rate for WLAN Channel Nodes” sixth national conference on recent innovation in science and engineering.
- Shyamala P, **Ramadevi P**, 2014, “Synthesis Synchronization for 2*2 MIMO WLAN System”, sixth national conference on recent innovation in science and engineering.
- Shyamala P, **Ramadevi P**, 2014, “Compact Clock Skew Scheme for FPGA Based Pipe Lined Circuits”, sixth national conference on recent innovation in science and engineering.

➤ **Sponsored Research Projects:**

- Project - MSME IDEA HACKATHON 2.0, approved by Government of India, Project value- Rs.15, 00,000 in the year 2023-2024.

➤ **List of Seminar / Short Term Course /FDP/ Workshop organized:**

Organized: Seminar

- Speech Processing and its Applications 23.07.2010 to 24.07.2010, BIT Campus, Trichy. Sponsored by Anna University Chennai.
- Internet Programming, 08.10.2013 to 09.10.2013, BIT Campus, Trichy. Sponsored by TEQIP- II
- Strategy Technical Quality Improvement, 16-02-2013, BIT Campus, Trichy. Sponsored by Anna University Chennai.

- Nascent Technology in Signal Processing and Communication, 01-10-2016 to 02-10-2016. BIT Campus, Trichy. Sponsored by TEQIP- II.
- Campus to corporate through Entrepreneurship, 23-09-2016 to 24-09-2016, .BIT Campus, Trichy. Sponsored by TEQIP- II
- Strategical Technical Quality Management, 16-02-2016, BIT Campus, Trichy. Sponsored by TEQIP- II.
- IOT and Android Development using MATLAB Simulink, 22-07-2017, BIT Campus, Trichy. Sponsored by Anna University Chennai.

Organized: Symposium

- Symposium- TROY 18, 21-03-2018 to 22-03-2018, ECE Student Association
- Symposium- TROY 17, 28-03-2017 to 29-03-2017, TEQIP- II
- Symposium- TROY 16, 03-03-2016 to 04-03-2016, TEQIP- II
- Symposium- TROY 15, 30-03-2015 to 31-03-2015, TEQIP- II
- Symposium- ESPIL 14, 19-03-2015 to 20-03-2015, TEQIP- II
- Symposium- TROY 14, 30-10-2014, TEQIP- II

Organized: Workshop

- SDR and Cognitive Radio, 08-09-2018 to 09-09-2018, BIT Campus, Trichy. Sponsored by Anna University Chennai.

- Open Source Simulator for Wireless Communication, 03-05-2018 to 05-08-2018, BIT Campus, Trichy. Sponsored by Anna University Chennai.
- Advanced Microprocessor and Microcontroller, 17-08-2017 to 18-08-2017, BIT Campus, Trichy. Sponsored by Anna University Chennai.
- Advanced Communication System, 11-07-2017, BIT Campus, Trichy. Sponsored by Anna University Chennai.
- IoT –LoRa, 01-09-2017 to 02.09.17, BIT Campus, Trichy. Sponsored by Anna University Chennai.
- Graphical System Design Engineering Using Lab view, 09-08-2016 to 10-08-2016, BIT Campus, Trichy. Sponsored by TEQIP-II.
- VLSI Design Using Cadence Tools, 09-08-2016 to 10-08-2016, BIT Campus, Trichy. Sponsored by TEQIP-II.
- Renesas RL 78, 18-03-15, BIT Campus, Trichy. Sponsored by Anna University Chennai.
- VLSI Design Lab using CADENCE Tool, 26.03.2015 to 27.03.2015, BIT Campus, Trichy. Sponsored by TEQIP-II.
- System design using LABVIEW, 13.03.2014 to 14.03.2014, BIT Campus, Trichy. Sponsored by TEQIP-II.
- INTEL 8051 Microcontroller based System Design, 20-10-2013 to 21-10-2013, BIT Campus, Trichy. Sponsored by Anna University Chennai.
- MATLAB Simulink Applications, 31-01-2013 to 01-02-2013 BIT Campus, Trichy. Sponsored by Anna University Chennai.

- RENESANS RL78 Microcontroller, 05-05-2011 BIT Campus, Trichy. Sponsored by Anna University Chennai.
- Emerging Trends on High and FPGA, 10-12-2010 to 12-12-2010, Anna University Trichy
- National level workshop in “Emerging Trends on Bio signal Processing, Biomaterials” 07-08-2019 to 08-08-2019, BIT Trichy
- National Level Workshop in “Advanced Signal Processing using Matlab, 09-09-2019 to 09-09-2019, BIT Trichy
- Analog and Digital Circuits Simulation using Labview, 12-09-2019 to 12-09-2019, BIT Trichy
- Advanced Communication System using Labview, 13-09-2019 to 13-09-2019, BIT Trichy

➤ **List of Seminar / Short Term Course /FDP/ Workshop attended:**

Attended: Faculty development Programmes (not less than one week duration)

- Orientation Course, 02-05-2009 to 29-05-2009, Bharathidasan University Trichy
- Engineering Practices on Fuzzy Logic, Neural Network, 15-06-2009 to 27-06-2009, NIT Trichy
- Cryptography and Network Security, 17-12-2011 to 23-12-2011, MIT Chennai
- VLSI Design, 12-12-2012 to 18-12-2012, Regional Centre Coimbatore
- Smart Teaching for the Net Generation, 09-12-2013 to 23-12-2013, National Institute of Technical Teachers

- A Compendium in High Speed and Mobile Networks, 05-08-2013 to 11-08-2013, BIT Campus Trichy
- Computer Networks, 30-06-2013 to 05-06-2013, BIT Campus Trichy
- Cyber Security, 10-07-2014 to 20-07-2014, organized by IIT Bombay at Sastra
- Digital Communication, 19-06-2015 to 25-06-2015, BIT Campus Trichy
- Emerging Trends in Mathematical Modeling and Simulink, 08-05-2015 to 14-05-2015, BIT Campus Trichy
- Enhancing Classroom Communication, 18-07-2016 to 31-07-2016, BIT Campus Trichy
- Top Tech Trends Driving the Future of Information Technology, 30-06-2016 to 13-07-2016, BIT Campus Trichy
- Deep Learning and Applications, 09-12-2019 to 13-12-2019, MEITY GOVERNMENT OF INDIA
- Cryptography and Network Security, 01-01-2020 to 30-04-2020, NPTEL ONLINE MODE
- Communication and Network Technologies, 08-06-2020 to 26-06-2020, BSNL TIRUCHIRAPPALLI

Attended: Seminar

- Cloud Computing, 15-09-16 to 16-09-16, BIT Trichy
- MIMO Wireless Communication and Future Networks, 02-12-13 to 03-12-13, RMK Engineering College Chennai
- MIMO Wireless Communication and Standards, 28-09-12 to 29-09-12, KLN College of Information Technology

- Design Issus and Research Challenges of OFD Wireless Network, 04-02-11 to 05-02-11, BIT Trichy
- Software Defined Radio, 27-08-10 to 28-08-10, Oxford Engineering College Trichy
- Speech Processing and its Applications, 23-07-10 to 24-07-10, BIT Trichy
- Analog Discovery Kit, 07-06-2019 to 07-06-2019, BIT Trichy
- Image and Pattern Recognition, 27-12-16 to 27-12-16, BIT Trichy,
- Research Methodology, 29-01-15 to 29-01-15, CFD Anna University Chennai
- Wireless Network and Security, 24-02-2015 to 24-02-2015, Dept. of IT CEG Campus Anna University
- Cyber Security, 06-11-14, IIT Bombay at Sastra University
- RL78 Microcontrollers, 06-11-14, Dept of ECE CEG Campus Anna University
- Cognitive Radio and its Hardware Implementation, 23-12-14 to 24-12-14, NIT Trichy
- Network On chip Architecture, 20-02-12 to 25-02-12, Oxford Engineering College Trichy
- Network Security, 20-07-12, Anna University Chennai
- VLSI Applications in High Data Rate Communication Processing using ASIC Design, 12-11-10 to 13-11-10, Oxford Engineering College Trichy
- VLSI Design Technique and Applications, 03-12-09 to 04-12-09, KLN College of Engineering
- Image and Pattern Recognition, 27-12-06, BIT Trichy

➤ **National / International Conferences organized / Participated:**

Organized: Conference

- Nascent Technologies in Signal Processing and communication, 01.09.2016 to 02.09.2016, Sponsored by TEQIP II
- International Conference on Emerging Trends in Electrical and Electronics and Communion Engineering, 24-10-2016 to 26-10-2016, TEQIP II
- Nascent Technologies in Signal Processing and communication, 27.11.201 to 28.11.2014 Sponsored by TEQIP II

➤ **Professional recognitions:** (details like chairperson/member of a committee, reviewer, editor ...etc)

- Overall ECE course coordinator, 2003 to 2012
- Head of the Department i/c, 2012 to 2019
- Head of the Department i/c, 2023 to till date
- Camp Officer University Central Valuation, Even semester 2023
- Member - Board of Studies for ECE, 2006 to 2008
- Officer in - charge of National Service Scheme, 2003 to 2006
- Chief Superintendent of University Examinations, 2015 to 2016
- AUCAI Inspection Affiliation Academic Year Member of Inspection Committee, 2017 and 2022

- Apex Committee Member Periyar Centenary Girls Polytechnic College, From 2016
- Centre for Research Constitution of Inspection Committee Member, 2016
- Graduation Day Committee Member Prizes Awards and Medals, 2014 to 2017
- ECE Student Association, 2014 to till date.

➤ **Guest lecturers delivered:**

- Refresher Course in Theoretical Physics, Network Analysis and Synthesis, 13-02-2014, Organized by Academic Staff College Bharathidasan University
- Centre for Faculty Development Programme Anna University Chennai, Characteristics Impedance of Symmetrica Asymmetrical Networks, 17-06-2014, Organized by Jayaram college of Engineering.
- Two days work shop on Microprocessor and Microcontrollers, Microprocessor and Microcontrollers, February 2011, Organized by Short term courses, Anna University, Trichy
- Judge for the Mini project Exhibition, February 2011, Organized by Kaveri College of Engineering, Trichy
- Three days work shop on Emerging Trends on High End FPGAs, Applications of VLSI Technology, Organized by Department of ECE, Anna University Trichy.
- Network Security, 21-07-2017, Organized by Periyar Centenary Polytechnic College