CURRICULUM VITAE

Mrs.K.Padmaja
Teaching Fellow
Department of ECE
UCE – BIT Campus, Tiruchirappalli 620 024
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➢ **Research Area:**
  - Image Processing
  - VLSI Design
  - Soft Computing
  - Artificial Intelligence

➢ **Educational Qualifications:**

Qualification	College/University	Year of completion	Percentage	Class
Ph.D. (Image Processing)	Anna University, Chennai	Pursuing	-	-
PG (M.E., VLSI)	Oxford Engineering College, Trichy	2012	8.85	FWD
UG (B.E., ECE)	Jayaram College of Engg., and Tech, Trichy	2009	78	FC

Experience:

Name of Company / Institutions	Designation of Post	From	To	Experience in Year
UCE – BIT Campus, Tiruchirappalli	Teaching Fellow	24.07.2013	Till date	10.3 years

➢ **Awards / Honours Received:**

1. Anna University First Rank holder in ME-VLSI Design (2010-2012)
2. Received Best Paper Award for the paper entitled "Effects of False Positive in Cancer Diagnosis Using Mammogram", in TEQIP-II Sponsored International Conference on Emerging Trends in Electrical, Electronics and Communication Systems

(ICEEECS'16), organized by Dept. of ECE & EEE, Anna University, BIT Campus, Tiruchirappalli – 24.

3. Acquired 100% passing result in 3 consecutive years in OIC551-Bio Medical Instrumentation in the University Semester Examination held during 2019-20,2020-21 and 2021-22.

4. Acquired 100% passing result in Electronic Packaging and Testing in the University Semester Examination held during 2020-21and 2021-22.

5. Acquired 100% passing result in MG8591-Principles of Management in the University Semester Examination held during 2020-2021.

6. Acquired 100% passing result in EC8095-VLSI Design in the University Semester Examination held during 2019-2020.

7. Academic proficiency holder in both UG and PG and subject wise topper in PG.

➢ **Additional / Academic Responsibilities at University:**
   1. Has been First Year Induction Program Coordinator for the academic year 2022-2023.
   2. Has been Class Coordinator for 2022 passed out students.
   3. Being Department Result Analysis Coordinator from 2014 to till date.
   4. Being Department Minute taker from 2020 to till date.
   5. Has been Master of ceremonies in International Conference on Emerging Trends in Electrical, Electronics and Communication Engineering (ICEEECS'16) from 24.09.2016 to 26.09.2016, Organized by the Department of EEE and ECE, University College of Engineering, BIT Campus, Anna University, Trichy.
   6. Has been a creative designer for several Department related workshop invitations and certificates.

   Completed PG/UG Projects:
   a. Number of PG Projects Completed: NIL

   b. Number of UG Project Completed: 12 batches

➢ **List of Seminar / Short Term Course /FDP/ Workshop organized:**
   • Organized training program on DSP concepts and Applications on 23.8.2021 by Dr.P.Madhan Mohan, Jasmin Infotech, Chennai.

   • Organized Webinar on Cellular Mobile Communication 17.06.2020.

   • Organized Webinar on Python Programming-Practical Approach 12.06.2020.

   • Organized training program on Practical aspects of Networking and Its Certification on 10.2.2020 by Mr.V.N. Ramalingam, Founder and Director-Entrust Technology Services, Trichy.

- Co-organized National Conference on Nascent Technologies in Signal Processing and Communication Sponsored by TEQIP II and organized by the Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy on 01.09.2016 and 02.09.2016.

➢ **List of Seminar / Short Term Course /FDP/ Workshop attended:**
- Attended three days International Online Workshop on "Academic Writing: Ethical Issues" from 16th to 18th September '23 Organized by Global Institute of Statistical Solutions (GISS).
- Attended Journal Citation Reports (JCR) Training & Certification Program – 2022 on 10 August 2022 organized by Clarivate
- Participated in the National Level Five-Days Online Workshop on "MATLAB and its Applications in Image Processing and Communication" conducted by the Department of Electronics and Communication Engineering, SRM TRP Engineering College, Tiruchirappalli from 21.02.2022 to 25.02.2022.
- Attended a two weeks online Faculty Development Programme (FDP) on "Speech, Audio and Image Processing using Deep Learning Techniques: Research Issues, Innovation and Applications" (07.07.2021- 17.07.2021) Organized by University College of Engineering, Ramanathapuram – 623513, Tamil Nadu in association with E & ICT Academy, NIT Warangal
- Participated in Faculty Development Programme from 10.08.2020 to 14.08.2020 on LaTeX organized by University College of Engineering, Bharathidasan Institute of Technology (BIT) Campus, Tiruchirappalli with course material provided by Spoken Tutorial Project, IIT Bombay funded by the National Mission on Education through ICT, MHRD, Govt. of India.
- Attended Technical Webinar series'20 on the topic Research Methodology organized by Mount Zion College of Engineering and Technology on 14.05.2020.
- Participated in a One-week Faculty Development Programme on "Python Programming with Industry Perspective" jointly organized by Electronics and ICT Academies held from 02.12.2019 to 06.12.2019 through National Knowledge Network under the "Scheme of Financial Assistance for setting up of Electronics and ICT Academies" by the Ministry of Electronics and Information Technology (MeitY), Governmnet of India.

- Attended one day workshop on 23.10.2019 jointly organized by Centre for Development of Tamil in Engineering and Technology, Anna University, Chennai and University College of Engineering, Anna University, Trichy.
- Attended one day workshop on "Analog Discovery Kit" on 07.06.2019 organized by Department of ECE, University College of Engineering, Anna University, Trichy
- Attended CSIR Sponsored National Level Seminar on "Artificial Neural Network for Solving Medical Diagnosis Problems" Organized by Department of ECE, N.S.N College of Engineering and Technology, Karur held on 06.02.2019.
- Participate in One day National Level Workshop on "Sensitization on Intellectual Property Rights for Professional Innovators in ICTE" sponsored by Ministry of Electronics and Information Technology (MeitY) held on 31.08.2018, Organized by the Department of ICT, Selvam College of Technology, Namakkal.
- Attended ICMR Sponsored Nation Seminar on "Medical Image Analysis" Organized by the Department of ECE at K.Ramakrishnan College of Engineering on 11.08.2017 and 12.08.2017.
- Attended one day National Level Workshop on "IOT and Android App Development using MATLAB/Simulink" on 22.07.2017 organized by Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy.
- Attended one day Faculty Training Program in "Advanced Communication System" on 11.07.2017 organized by Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy.
- Attended a National Level Workshop on "Medical Image Analysis and Detection Techniques with Advanced Algorithms" conducted in association with IIP Cell on 12.08.2016 and 13.08.2016 at Kongu Engineering College, Perundurai, Erode.
- Attended a TEQIP II Sponsored Faculty Development Programme on "Heuristic Approach for Information and Communication Engineering Research Problems" from 15.06.2016 to 28.06.2016.
- Attended TEQIP II sponsored course on "Faculty Development Programme" organized by BIT Campus, Anna University, Trichy in association with Confederation of Indian Industry.
- Attended TEQIP II Sponsored One week Faculty Development Programme on "EC6501-Digital Communication (DC-2015)" from 19.06.2015 to 25.06.2015 organized by

Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy.

- Attended two days workshop on "VLSI Design Lab Using Cadence Tool" from 26.03.2015 to 27.03.2015 organized by Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy

- Attended two days workshop on DIGITAL IMAGE PROCESSING, National Institute of Technology, Tiruchirappalli from 04.03.2012 to 05.03.2012.

- Attended two days workshop on EMERGING TRENDS ON HIGH-END FPGAs, Anna University of Technology, Tiruchirappalli from 10.12.2010 to 12.12.2010

- Attended two days workshop on VLSI APPLICATIONS IN HIGHER DATA RATE COMMUNICATIONS AND SIGNAL PROCESSING USING ASIC TOOLS, CoreEL Technologies, Bangalore 12.07.2010 to 13.07.2010.

➢ **National / International Conferences Organized / Participated:**

- Presented paper on "Wavelet Feature-Based Microcalcification Detection in Mammogram" in the Advances in Intelligent Systems and Computing, Springer-Computational Algorithm for AI Technology Proceedings of ICAIECES-2020, 6th International Conference on Artificial Intelligence and Evolutionary Computations in Engineering Systems held on 18.12.2020 and 19.12.2020.

- Presented paper on "Effects of False Positive in Cancer Diagnosis using Mammogram" in the International Conference on Emerging Trends in Electrical, Electronics and Communication Engineering (ICEEECS'16) from 24.09.2016 to 26.09.2016, Organized by the Department of EEE and ECE, University College of Engineering, BIT Campus, Anna University, Trichy.

- Presented a paper titled "A study on Medical Image Classification Algorithms" in the TEQIP II Sponsored Two days National Conference on Nascent Technologies in Signal Processing and Communication organized by the Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy on 01.09.2016 and 02.09.2016.

- Presented a paper titled "FPGA Based Real Time Face Detection using Adaboost and Histogram Equalization" in IEEE – International Conference on Advances in Engineering, Science and Management (ICAESM)-IEEE Xplore, Volume.2 PP. 111-115, from 30.03.2012 to 31.03.2012.

- Presented a paper titled "FPGA Based Real Time Face Detection" in National Conference in RECENT TRENDS IN COMPUTATIONAL INTELLIGENT TECHNIQUES IN POWER SYSTEM on18.02.2012.

- Presented a paper titled HAAR Feature based face detection using NIOS-II processor in National Conference KEYNOTE-2012 from 09.02.2012 to10.02.2012.

➢ **Professional recognitions:** (details like chairperson/member of a committee, reviewer, editor …etc)

- Has been a Mentor for four teams for100 days long COVID-19 National Bioinformatics Online Hackathon from 29.04.2020 to 03.08.2020 for mentoring full stackers in developing Android App and mobile responsive web for all 36 states and union territory.
- Contributed as a Judge for the event "Paper Presentation" in MICFA'19, A National Level Symposium during 26.09.2019 and 27.09.2019 conducted by the Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy.
- Has been a Judge of "ADAPT Tunes" event in FIESTA'17 on 28.03.2017 held at University College of Engineering, BIT Campus, Anna University, Trichy.

(A) Chairperson for conferences / Seminar / Technical symposia:

(B) Guest lectures delivered:

- Delivered a Lecture on "Department Specific Activities" during the First Year Induction Programme on 22.09.2023 organized by Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy

- Delivered a Lecture on "Department Specific Activities" during the First Year Induction Programme on 25.11.2022 organized by Department of ECE, University College of Engineering, BIT Campus, Anna University, Trichy